

RESPONSE UNDER 37 C.F.R. § 1.116 - EXPEDITED PROCEDURE

Serial Number: 09/382,442

Filing Date: August 25, 1999

Title: METHOD FOR REDUCING SINGLE BIT DATA LOSS IN A MEMORY CIRCUIT

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Dkt: 303.522US1

as hydrogen. Thus, there is no suggestion that single bit data loss is reduced by deuterium because the references cited provide no description of how deuterium affects erasing operations. The Examiner stated in the Advisory Action that the Lisenker reference does discuss FLASH memory and the effectiveness of deuterium in erasing operations. The Applicant respectfully requests that the Examiner provide the cite in the Lisenker reference upon which he relies.

CONCLUSION

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney at (612) 373-6976 to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

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By their Representatives,

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Date 11 Feb 02

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Attn: Box RCE, Commissioner for Patents, Washington, D.C. 20231, on this 11 day of February, 2002.

Name

Tina Pugh

Signature

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